

این دیتاشیت توسط توکل الکترونیک تهیه و ارائه شده است.
جهت مشاهده قیمت، موجودی و ثبت سفارش این قطعه به وبسایت توکل الکترونیک مراجعه فرمایید.



مشاهده قیمت و خرید
برای خرید و مشاهده سایر محصولات کلیک کنید



NIKO-SEM

N-Channel Enhancement Mode
Field Effect Transistor

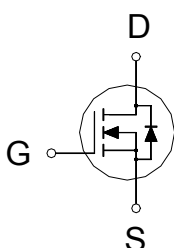
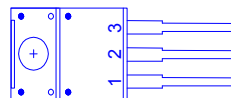
P1308ATG

TO-220

Halogen-Free & Lead-Free

PRODUCT SUMMARY

$V_{(BR)DSS}$	$R_{DS(ON)}$	I_D
75	$13m\Omega$	62A



1. GATE
2. DRAIN
3. SOURCE

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ Unless Otherwise Noted)

PARAMETERS/TEST CONDITIONS		SYMBOL	LIMITS	UNITS
Gate-Source Voltage		V_{GS}	± 20	V
Continuous Drain Current	$T_C = 25^\circ\text{C}$	I_D	62	A
	$T_C = 100^\circ\text{C}$		39	
Pulsed Drain Current ¹		I_{DM}	200	
Avalanche Current		I_{AS}	53	
Avalanche Energy	$L = 0.1\text{mH}$	E_{AS}	140	mJ
Power Dissipation	$T_C = 25^\circ\text{C}$	P_D	96	W
	$T_C = 100^\circ\text{C}$		38	
Operating Junction & Storage Temperature Range		T_j, T_{stg}	-55 to 150	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS

THERMAL RESISTANCE	SYMBOL	TYPICAL	MAXIMUM	UNITS
Junction-to-Case	$R_{\theta JC}$		1.3	$^\circ\text{C} / \text{W}$
Junction-to-Ambient	$R_{\theta JA}$		62.5	

¹Pulse width limited by maximum junction temperature.

ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$, Unless Otherwise Noted)

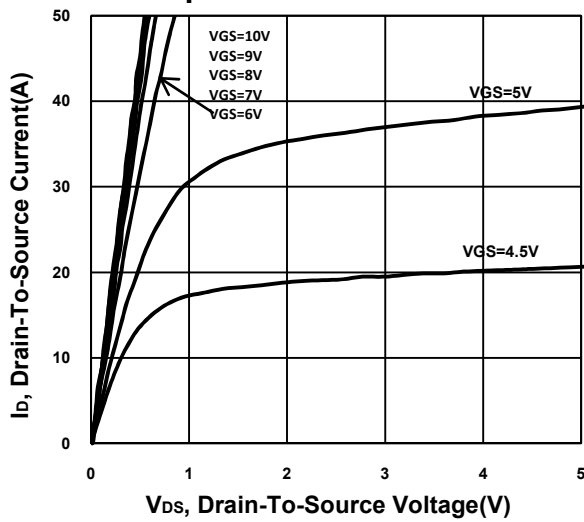
PARAMETER	SYMBOL	TEST CONDITIONS	LIMITS			UNIT
			MIN	TYP	MAX	
STATIC						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	75			V
Gate Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	1.5	2.3	4.0	
Gate-Body Leakage	I_{GSS}	$V_{DS} = 0V, V_{GS} = \pm 20V$			± 250	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = 60V, V_{GS} = 0V$			1	μA
		$V_{DS} = 60V, V_{GS} = 0V, T_J = 125^\circ C$			10	
On-State Drain Current ¹	$I_{D(ON)}$	$V_{DS} = 10V, V_{GS} = 10V$	200			A
Drain-Source On-State Resistance ¹	$R_{DS(ON)}$	$V_{GS} = 10V, I_D = 40A$		9.5	13	$m\Omega$
Forward Transconductance ¹	g_{fs}	$V_{DS} = 10V, I_D = 40A$		33		S

DYNAMIC						
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 25V, f = 1MHz		3370		pF
Output Capacitance	C _{oss}			279		
Reverse Transfer Capacitance	C _{rss}			276		
Total Gate Charge ²	Q _g	V _{DS} =40V, V _{GS} = 10V, I _D = 40A		79		nC
Gate-Source Charge ²	Q _{gs}			17.2		
Gate-Drain Charge ²	Q _{gd}			33		
Turn-On Delay Time ²	t _{d(on)}	V _{DD} = 40V, I _D ≅ 40A, V _{GS} = 10V, R _{GS} = 25 Ω		43		nS
Rise Time ²	t _r			212		
Turn-Off Delay Time ²	t _{d(off)}			293		
Fall Time ²	t _f			147		
SOURCE-DRAIN DIODE RATINGS AND CHARACTERISTICS (T _J = 25 °C)						
Continuous Current	I _S				62	A
Forward Voltage ¹	V _{SD}	I _F = 40A, V _{GS} = 0V			1.4	V
Reverse Recovery Time	t _{rr}	I _F = 40A, dI _F /dt = 100A / μS		45		nS
Reverse Recovery Charge	Q _{rr}			70		nC

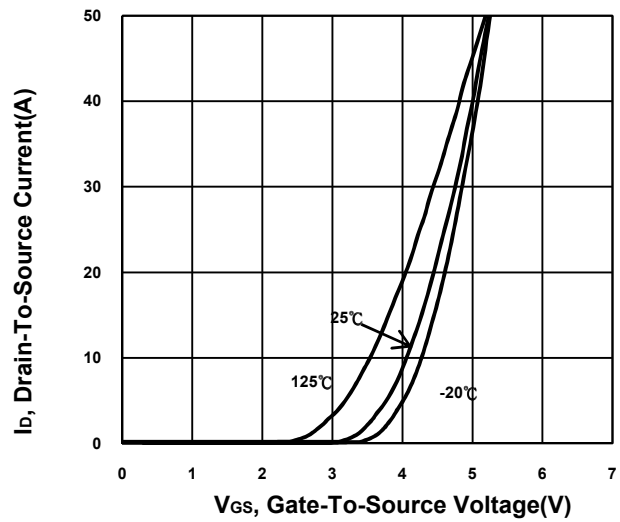
¹Pulse test : Pulse Width $\leq 300 \mu sec$, Duty Cycle $\leq 2\%$.

²Independent of operating temperature.

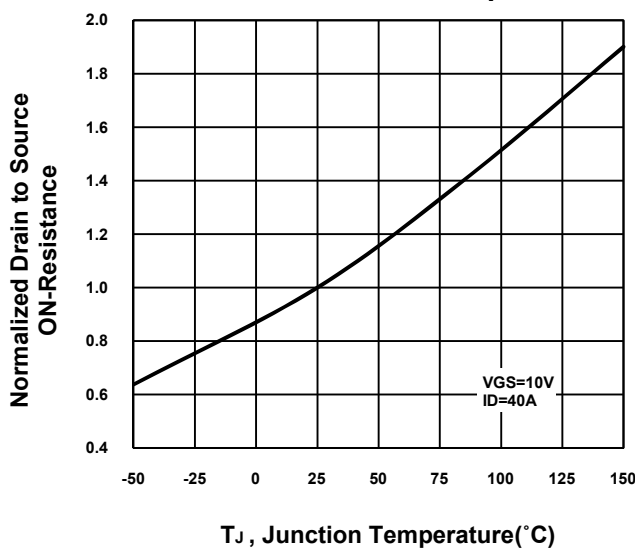
Output Characteristics



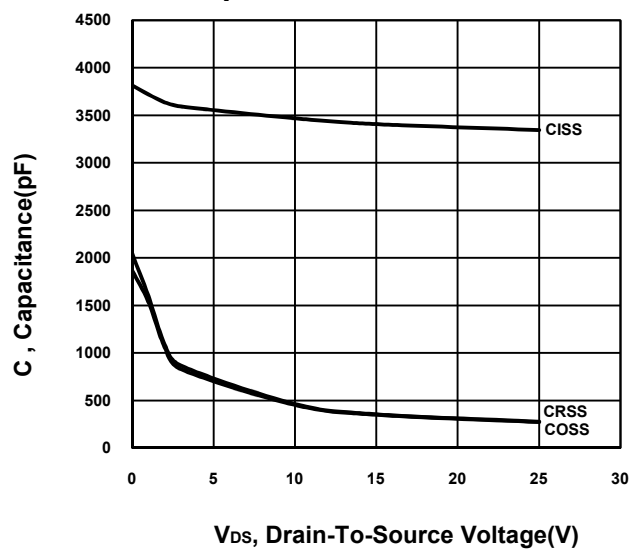
Transfer Characteristics



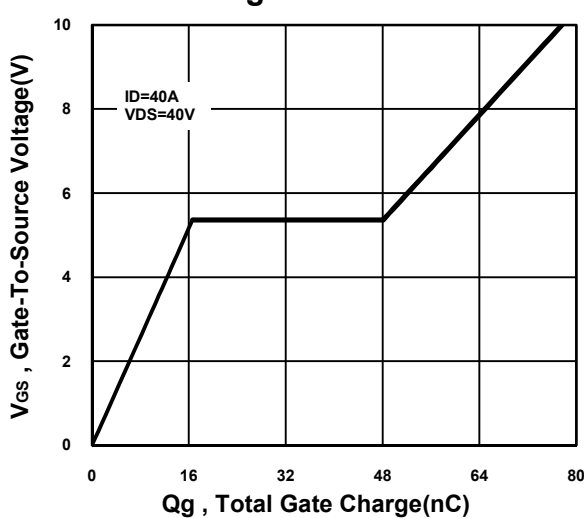
On-Resistance VS Temperature



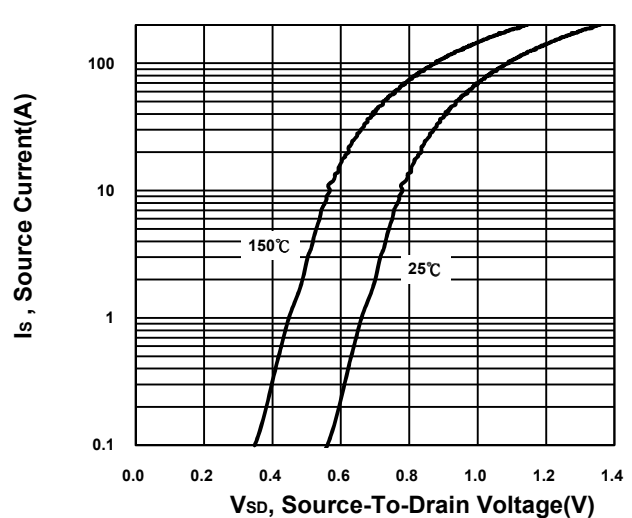
Capacitance Characteristic



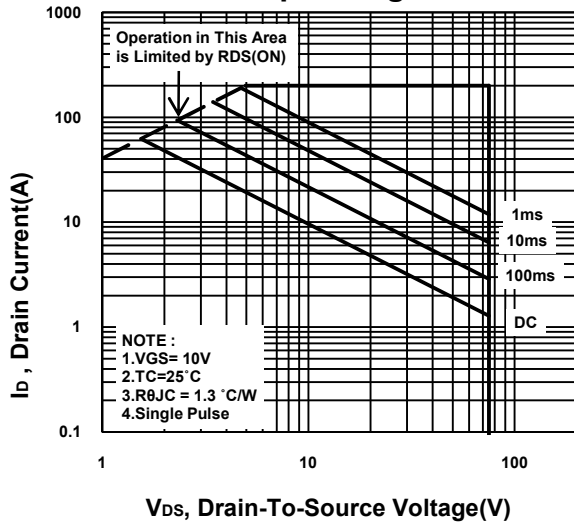
Gate charge Characteristics



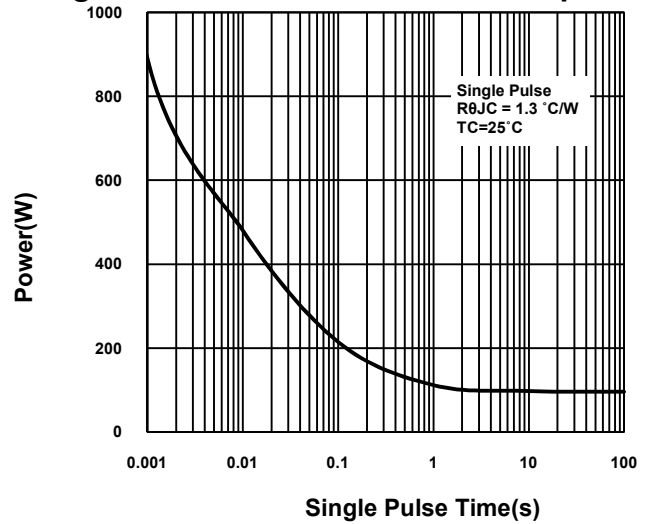
Source-Drain Diode Forward Voltage



Safe Operating Area



Single Pulse Maximum Power Dissipation



Transient Thermal Response Curve

