TAVAKOL ELECTRONIC
توکل الکترونیک
مرجع تخصصی تأمین قطعات الکترونیکی اورجینال
این دیتاشیت توسط توکل الکترونیک تهیه و ارائه شده است.
جهت مشاهده قیمت، موجودی و ثبت سفارش این قطعه به وب‌سایت توکل الکترونیک مراجعه فرمایید.
مشاهده قیمت و خرید
برای خرید و مشاهده سایر محصولات کلیک کنید

FAIRCHILD SEMICONDUCTOR®

IRF650B / IRFS650B

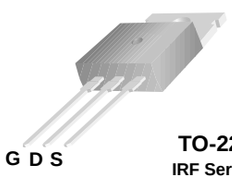
200V N-Channel MOSFET

General Description

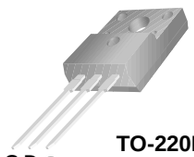
These N-Channel enhancement mode power field effect transistors are produced using Fairchild's proprietary, planar, DMOS technology.
This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switching DC/DC converters, switch mode power supplies, DC-AC converters for uninterrupted power supply and motor control.

Features

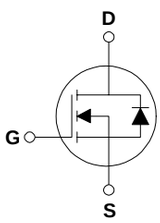
- 28A, 200V, $R_{DS(on)}$ = 0.085Ω @V_{GS} = 10 V
- Low gate charge (typical 95 nC)
- Low Crss (typical 75 pF)
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability



TO-220
IRF Series



TO-220F
IRFS Series



Absolute Maximum Ratings

T_C = 25°C unless otherwise noted

Symbol	Parameter		IRF650B	IRFS650B	Units
V_{DSS}	Drain-Source Voltage		200		V
I_D	Drain Current - Continuous (T_C = 25°C)		28	28 *	A
	- Continuous (T_C = 100°C)		17.7	17.7 *	A
I_{DM}	Drain Current - Pulsed	(Note 1)	112	112	A
V_{GSS}	Gate-Source Voltage		± 30		V
E_{AS}	Single Pulsed Avalanche Energy	(Note 2)	600		mJ
I_{AR}	Avalanche Current	(Note 1)	28		A
E_{AR}	Repetitive Avalanche Energy	(Note 1)	15.6		mJ
dv/dt	Peak Diode Recovery dv/dt	(Note 3)	5.5		V/ns
P_D	Power Dissipation (T_C = 25°C)		156	50	W
	- Derate above 25°C		1.25	0.4	W/°C
T_J, T_{STG}	Operating and Storage Temperature Range		-55 to +150		°C
T_L	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds		300		°C

* Drain current limited by maximum junction temperature.

Thermal Characteristics

Symbol	Parameter	IRF650B	IRFS650B	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case Max.	0.8	2.51	°C/W
$R_{\theta CS}$	Thermal Resistance, Case-to-Sink Typ.	0.5	--	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient Max	62.5	62.5	°C/W

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Electrical Characteristics

T_C = 25°C unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
Off Characteristics						
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS} = 0 V, I_D = 250 μA	200	--	--	V
ΔBV_{DSS} / ΔT_J	Breakdown Voltage Temperature Coefficient	I_D = 250 μA, Referenced to 25°C	--	0.2	--	V/°C
I_{DSS}	Zero Gate Voltage Drain Current	V_{DS} = 200 V, V_{GS} = 0 V	--	--	10	μA
		V_{DS} = 160 V, T_C = 125°C	--	--	100	μA
I_{GSSF}	Gate-Body Leakage Current, Forward	V_{GS} = 30 V, V_{DS} = 0 V	--	--	100	nA
I_{GSSR}	Gate-Body Leakage Current, Reverse	V_{GS} = -30 V, V_{DS} = 0 V	--	--	-100	nA
On Characteristics						
$V_{GS(th)}$	Gate Threshold Voltage	V_{DS} = V_{GS}, I_D = 250 μA	2.0	--	4.0	V
$R_{DS(on)}$	Static Drain-Source On-Resistance	V_{GS} = 10 V, I_D = 14 A	--	0.071	0.085	Ω
g_{FS}	Forward Transconductance	V_{DS} = 40 V, I_D = 14 A (Note 4)	--	25	--	S
Dynamic Characteristics						
C_{iss}	Input Capacitance	V_{DS} = 25 V, V_{GS} = 0 V, f = 1.0 MHz	--	2600	3400	pF
C_{oss}	Output Capacitance		--	330	430	pF
C_{rss}	Reverse Transfer Capacitance		--	75	100	pF
Switching Characteristics						
$t_{d(on)}$	Turn-On Delay Time	V_{DD} = 100 V, I_D = 32 A, R_G = 25 Ω (Note 4, 5)	--	30	70	ns
t_r	Turn-On Rise Time		--	240	490	ns
$t_{d(off)}$	Turn-Off Delay Time		--	295	600	ns
t_f	Turn-Off Fall Time		--	195	400	ns
Q_g	Total Gate Charge	V_{DS} = 160 V, I_D = 32 A, V_{GS} = 10 V (Note 4, 5)	--	95	123	nC
Q_{gs}	Gate-Source Charge		--	13	--	nC
Q_{gd}	Gate-Drain Charge		--	43	--	nC
Drain-Source Diode Characteristics and Maximum Ratings						
I_S	Maximum Continuous Drain-Source Diode Forward Current		--	--	28	A
I_{SM}	Maximum Pulsed Drain-Source Diode Forward Current		--	--	112	A
V_{SD}	Drain-Source Diode Forward Voltage	V_{GS} = 0 V, I_S = 28 A	--	--	1.5	V
t_{rr}	Reverse Recovery Time	V_{GS} = 0 V, I_S = 32 A, dI_F / dt = 100 A/μs (Note 4)	--	220	--	ns
Q_{rr}	Reverse Recovery Charge		--	1.89	--	μC

Notes:
1. Repetitive Rating : Pulse width limited by maximum junction temperature
2. L = 1.15mH, I_{AS} = 28A, V_{DD} = 50V, R_G = 25 Ω, Starting T_J = 25°C
3. I_{SD} ≤ 32A, di/dt ≤ 300A/μs, V_{DD} ≤ BV_{DSS}, Starting T_J = 25°C
4. Pulse Test : Pulse width ≤ 300μs, Duty cycle ≤ 2%
5. Essentially independent of operating temperature

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Typical Characteristics

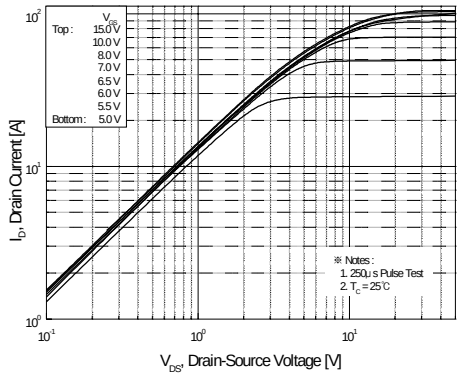

Figure 1. On-Region Characteristics

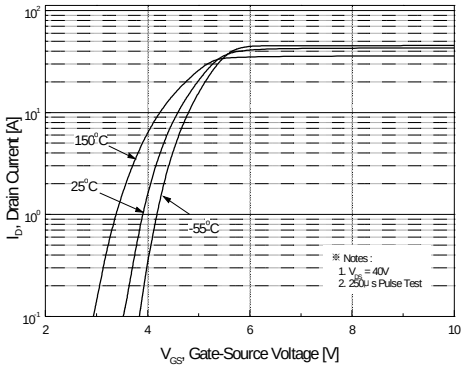

Figure 2. Transfer Characteristics

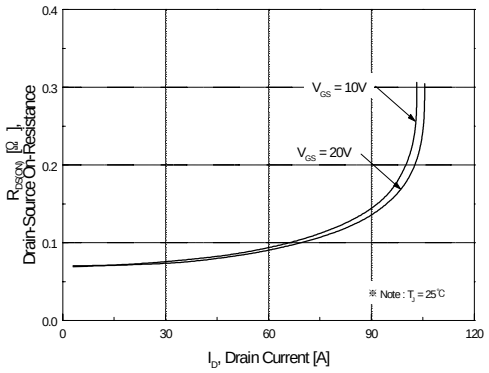

Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage

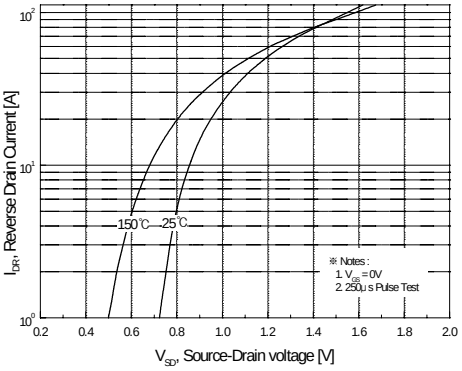

Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature

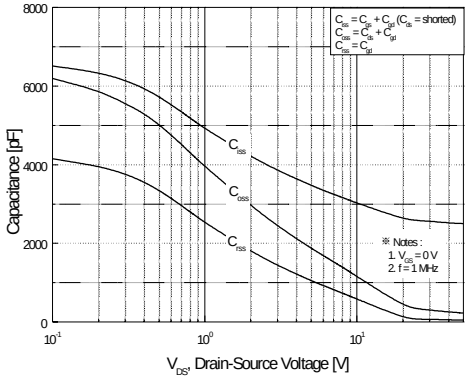

Figure 5. Capacitance Characteristics

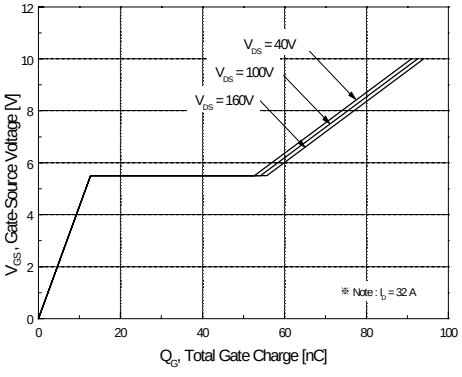

Figure 6. Gate Charge Characteristics

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Typical Characteristics (Continued)

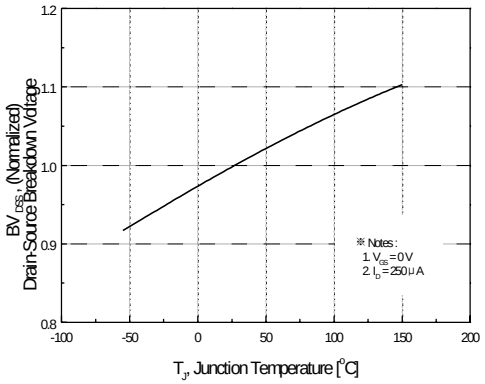

Figure 7. Breakdown Voltage Variation vs Temperature

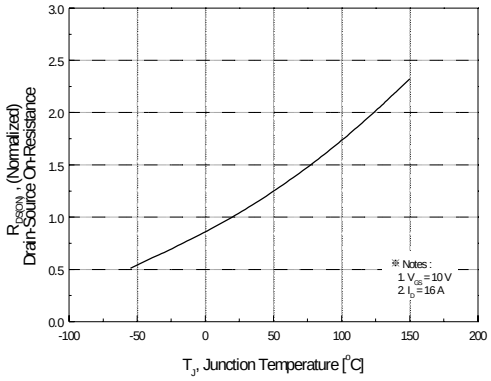

Figure 8. On-Resistance Variation vs Temperature

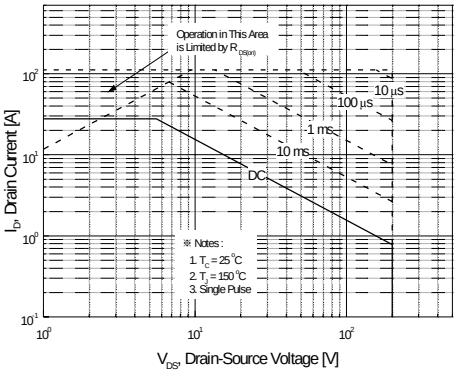

Figure 9-1. Maximum Safe Operating Area for IRF650B

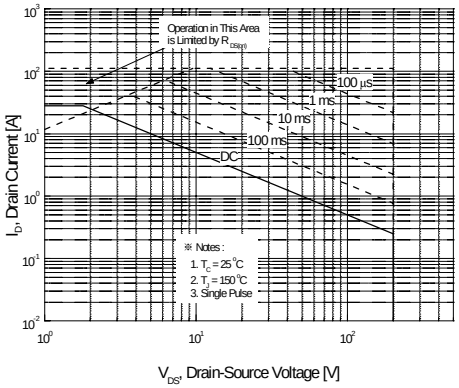

Figure 9-2. Maximum Safe Operating Area for IRFS650B

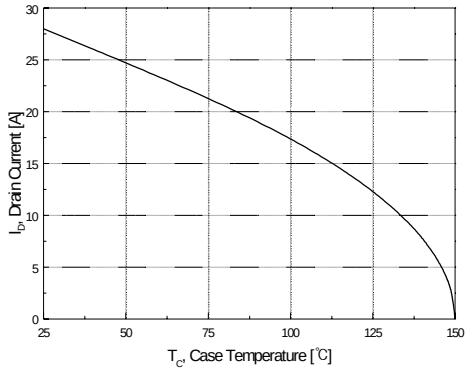

Figure 10. Maximum Drain Current vs Case Temperature

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Typical Characteristics (Continued)

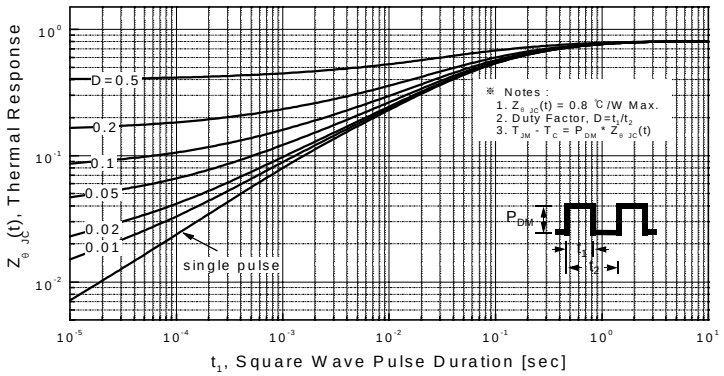

Figure 11. Transient Thermal Response Curve for IRF650B

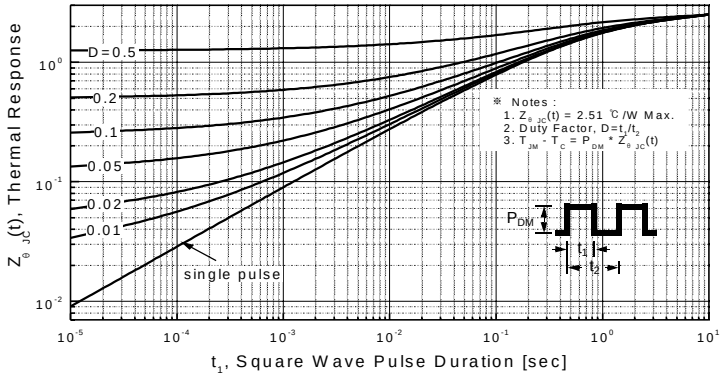

Figure 11. Transient Thermal Response Curve for IRFS650B

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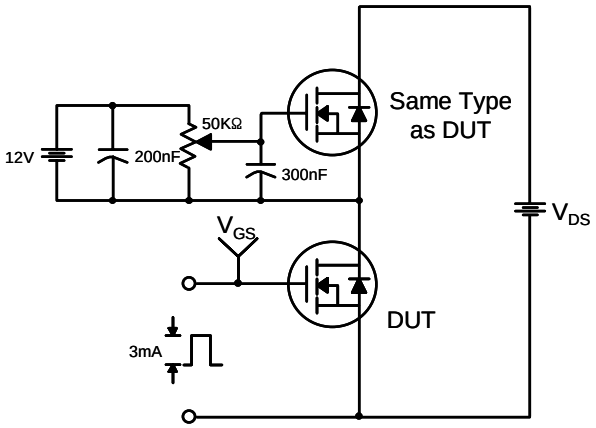
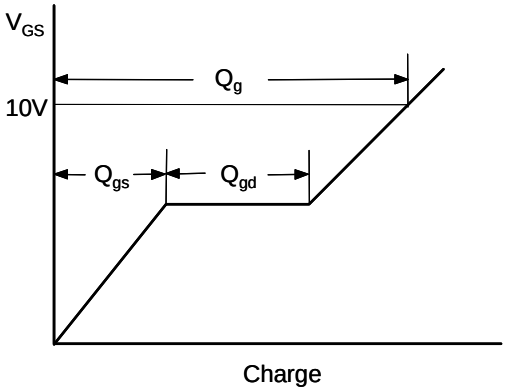
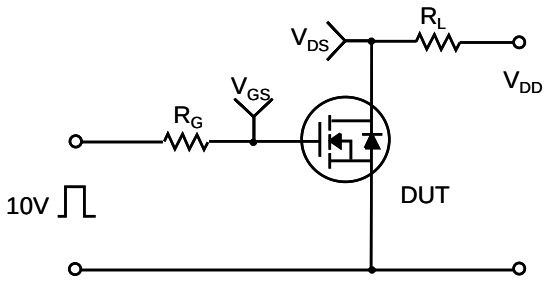
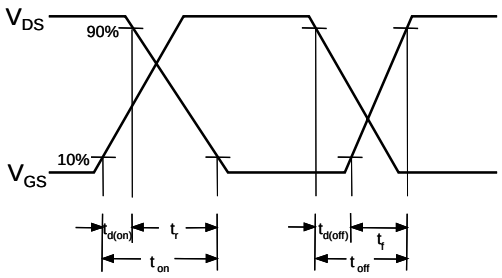
Gate Charge Test Circuit & Waveform



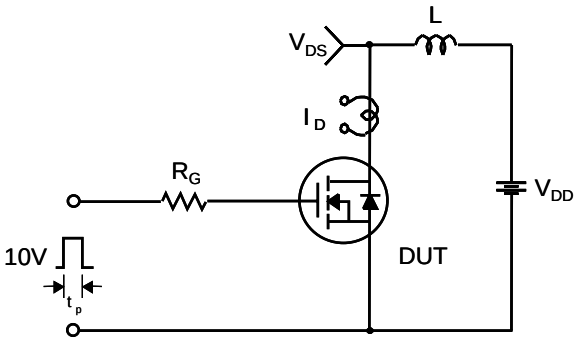


Resistive Switching Test Circuit & Waveforms

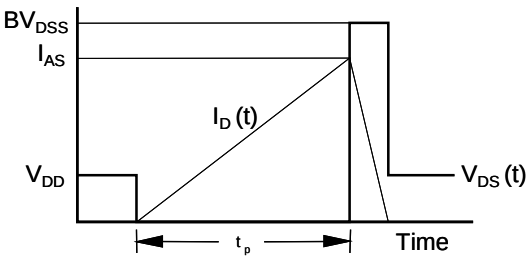




Unclamped Inductive Switching Test Circuit & Waveforms



$$E_{AS} = \frac{1}{2} L I_{AS}^2 \frac{BV_{DSS}}{BV_{DSS} - V_{DD}}$$



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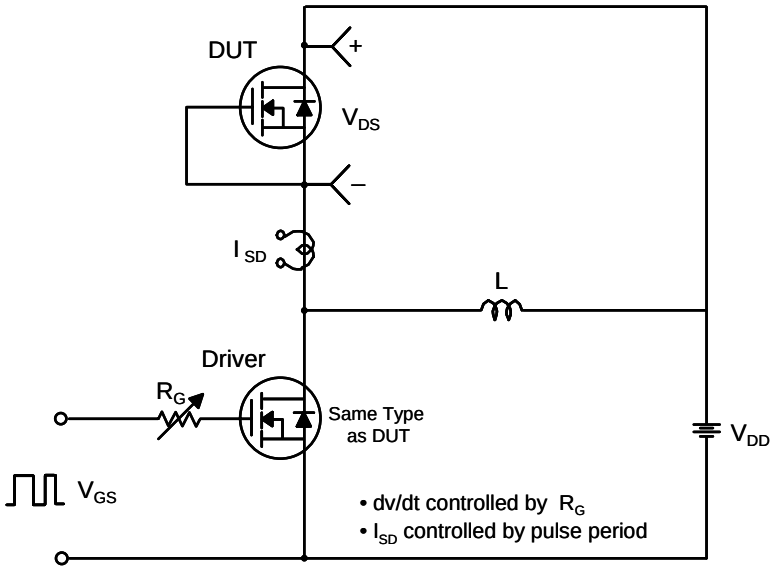
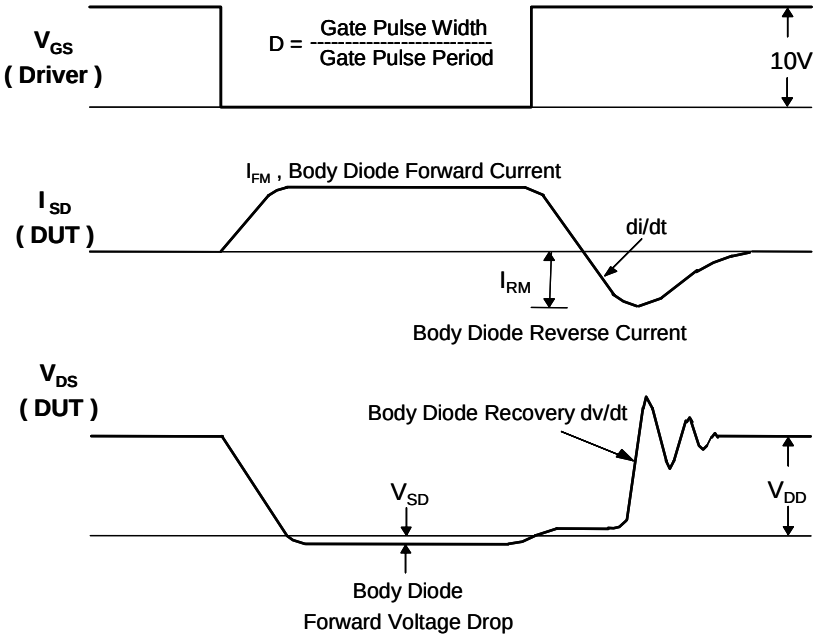
Peak Diode Recovery dv/dt Test Circuit & Waveforms





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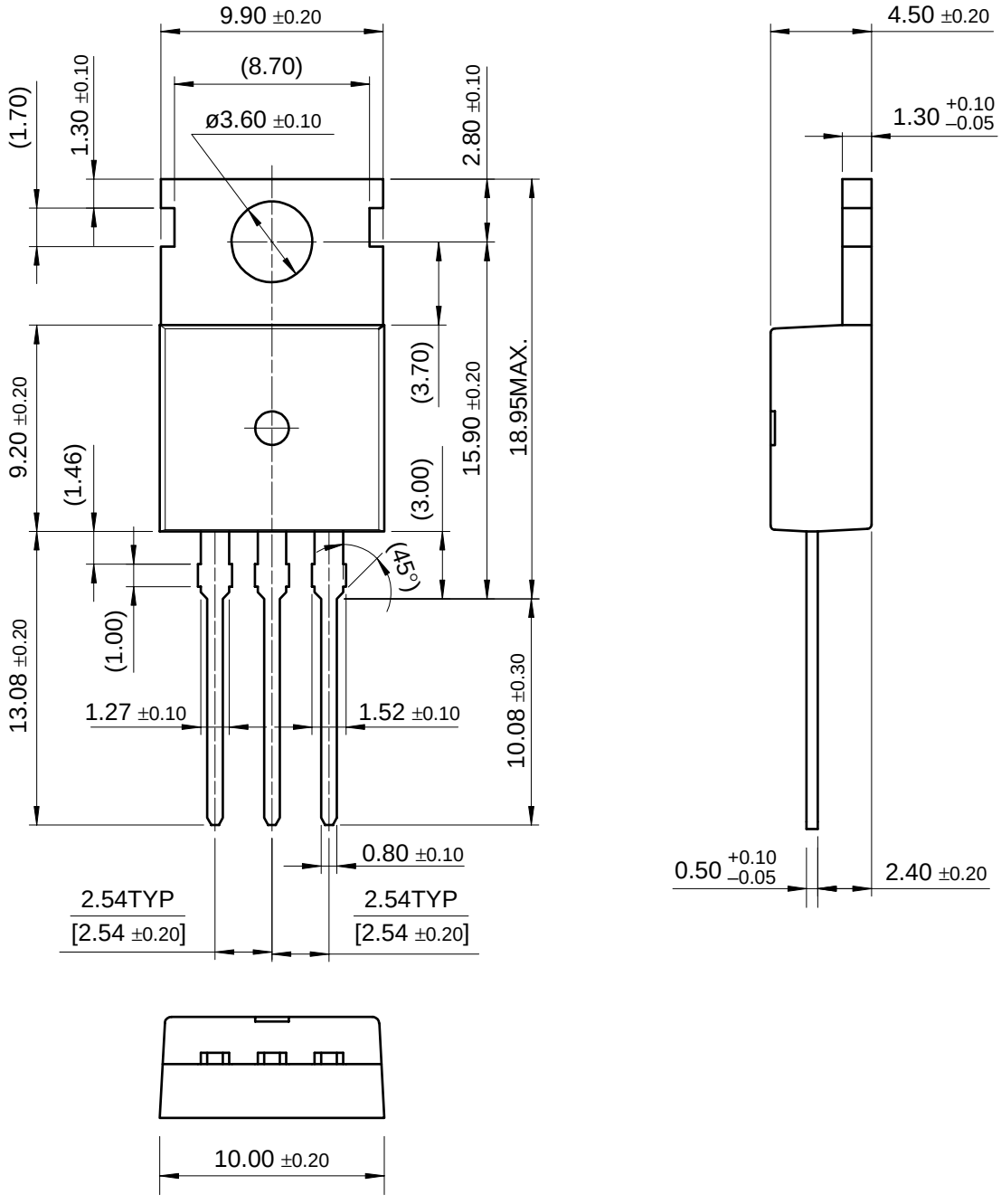
Package Dimensions

TO-220



Dimensions in Millimeters

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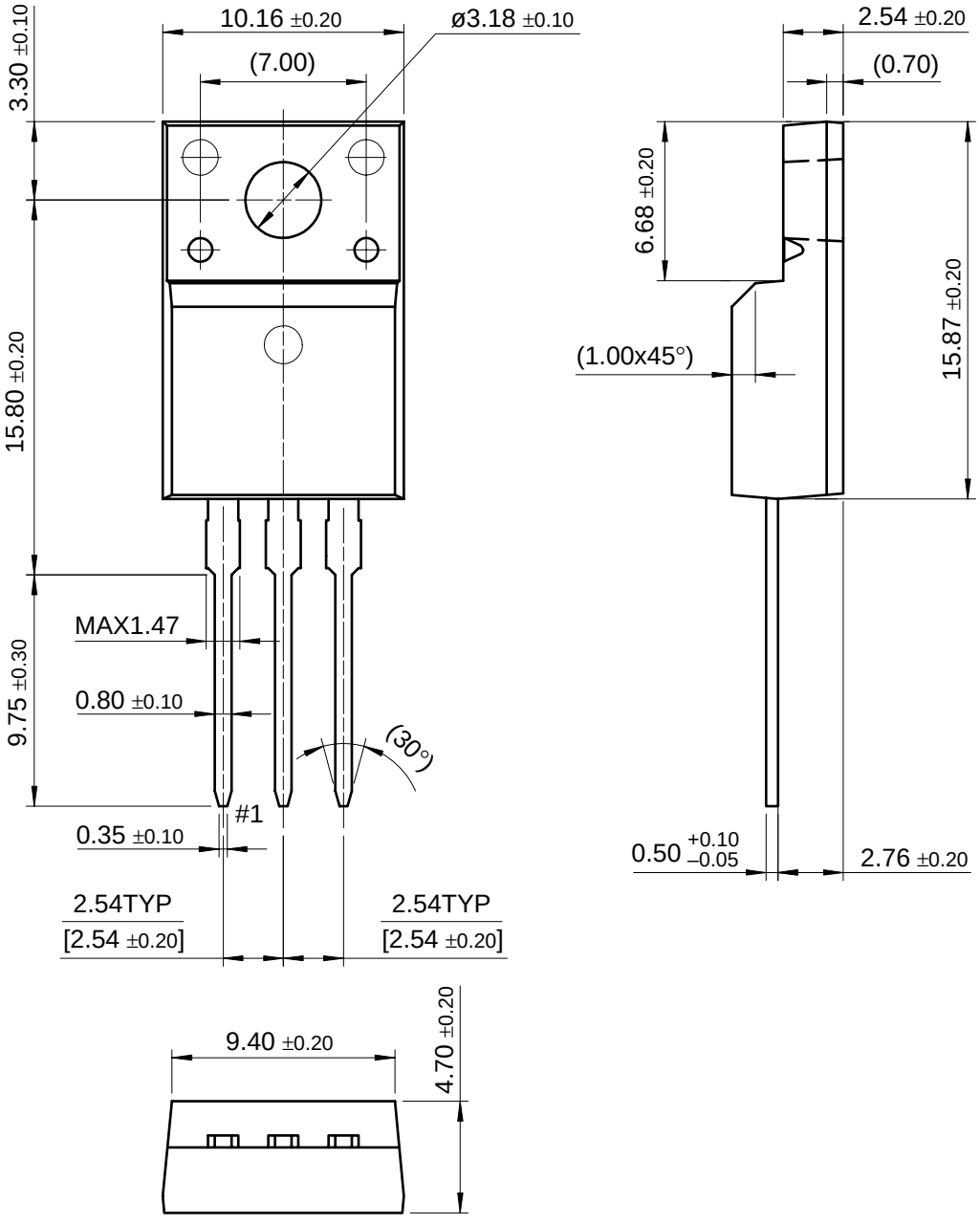
Package Dimensions (Continued)

TO-220F



Dimensions in Millimeters

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Definition of Terms

Datasheet Identification	Product Status	Definition
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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.

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